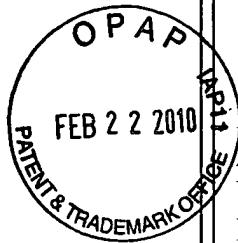


V64

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



In Re Application of: Reuven Bakalash et al.
Applicants : 10/579,682
U.S. Application No. : November 19, 2004
Filing Date : PC-BASED COMPUTING SYSTEM
Title of Invention : EMPLOYING PARALLELIZED GRAPHICS
PROCESSING UNITS (GPUS) INTERFACED
WITH THE CENTRAL PROCESSING UNIT
(CPU) USING A PC BUS AND A HARDWARE
GRAPHICS HUB HAVING A ROUTER
Group Art No. : 2628
Examiner : Hau H. Nguyen
Attorney Docket No. : 142-002USAC00

Honorable Commissioner
of Patents and Trademarks
Washington, D.C. 20231

RESPONSE TO OFFICE ACTION MAILED FEBRUARY 4, 2010

Sir:

In response to the Office Action mailed February 4, 2010, Applicants hereby submit a Terminal Disclaimer to overcome any double-patenting rejection over U.S. Application Nos: 11/977,178 filed October 23, 2007; 11/977,177 filed October 23, 2007; 11/977,164 filed October 23, 2007; 11/977,161 filed October 23, 2007; and 11/340,402 filed January 25, 2006.

In view therefore, of the Amendments and Remarks set forth above, the present invention defined by Claims 39-55 is firmly believed to be neither anticipated by, nor rendered obvious in view of the prior art of record, and that the present application is now in condition for allowance.

Favorable action is earnestly solicited.